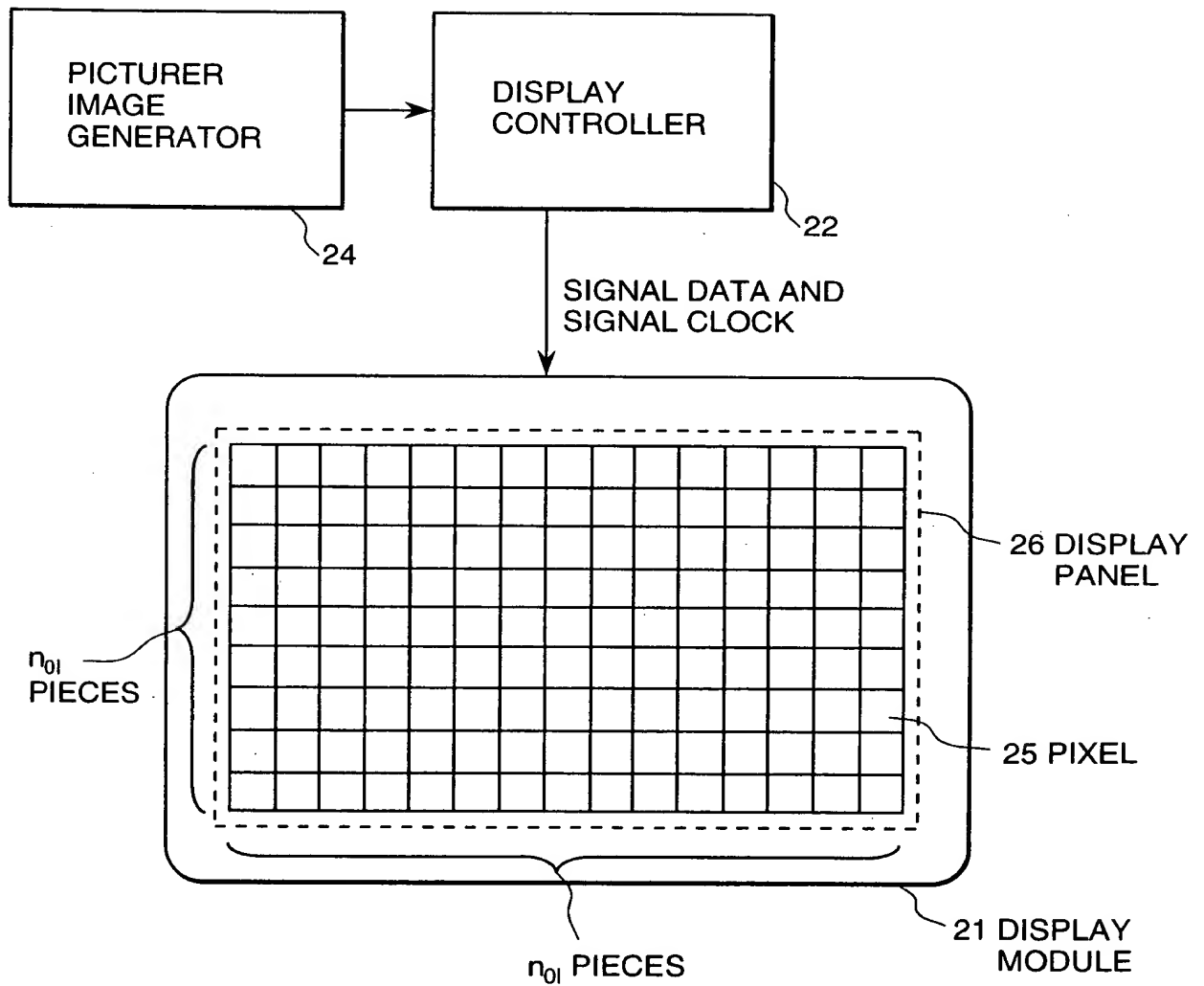




FIG. 2 -- PRIOR ART



The diagram illustrates the electrical architecture of a liquid crystal display system. At the top, a **PICTURE IMAGE GENERATOR** (34) sends data to a **DISPLAY CONTROLLER** (32). The **DISPLAY CONTROLLER** (32) is connected to a **SIGNAL DRIVER** (37) and a **COMMON ELECTRODE DRIVER** (39). The **SIGNAL DRIVER** (37) provides signals to a grid of pixel elements (41a, 41b, 41c, 41d) via horizontal lines (42). The **COMMON ELECTRODE DRIVER** (39) provides signals to the same grid via vertical lines (43). Each pixel element (41a-d) consists of a liquid crystal cell (45) and a switching element (46). The **SCAN DRIVER** (38) is connected to the vertical lines (43) and provides scan signals to the grid. The entire display area is enclosed in a dashed box (31), and the common electrode driver is shown as a separate block (39) at the bottom right.

COMMON ELECTRODE DRIVER